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## MAX77640/MAX77641 Evaluation Kit

Evaluates: MAX77640/MAX77641

### General Description

The MAX77640/MAX77641 evaluation kit (EV kit) is a fully assembled and tested printed circuit board (PCB) that demonstrates the MAX77640/MAX77641. The EV kit allows for easy evaluation of the MAX77640/MAX77641 resources, including the SIMO, LDO, GPIO, current sinks, and I<sup>2</sup>C interface.

Windows®-based software provides a user-friendly graphical interface (GUI) as well as a detailed register-based interface to exercise the features of the MAX77640/MAX77641.

Ordering Information appears at end of data sheet.

### Benefits and Features

- Easy to Use
  - GUI Drives I<sup>2</sup>C Interface
  - Red/Green/Blue LED
  - Fully Assembled and Tested
- Emulates System Loading
  - On-Board Electronic Load for LDO and SIMO Buck Boost Outputs
  - Electronic Load has Steady-State, Transient, and Random Modes
- Evaluates Both Push-Button and Slider-Switch On-Key Options

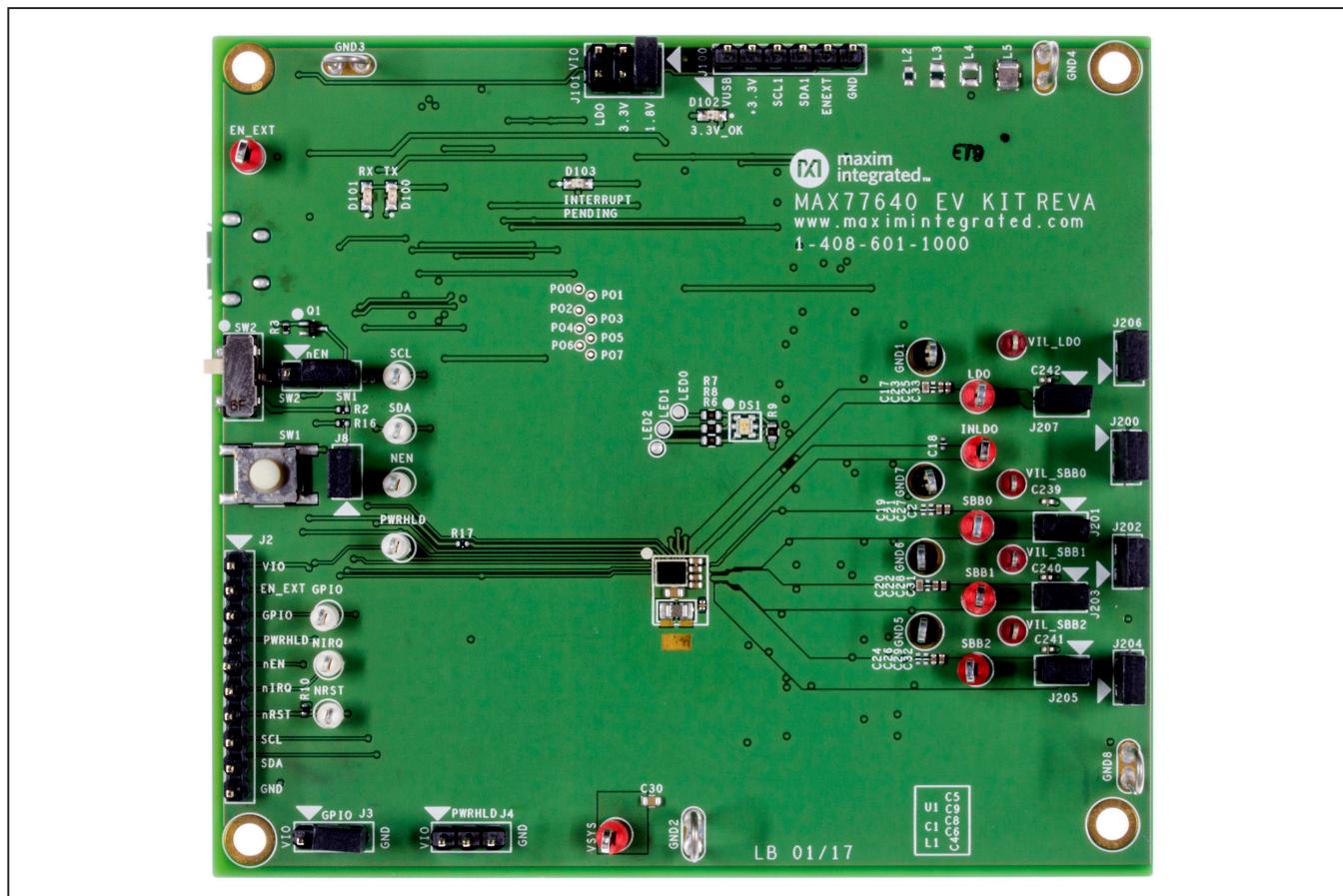


Figure 1. MAX77640/MAX77641 EV Kit Photo

Windows is a registered trademark and registered service mark of Microsoft Corporation.

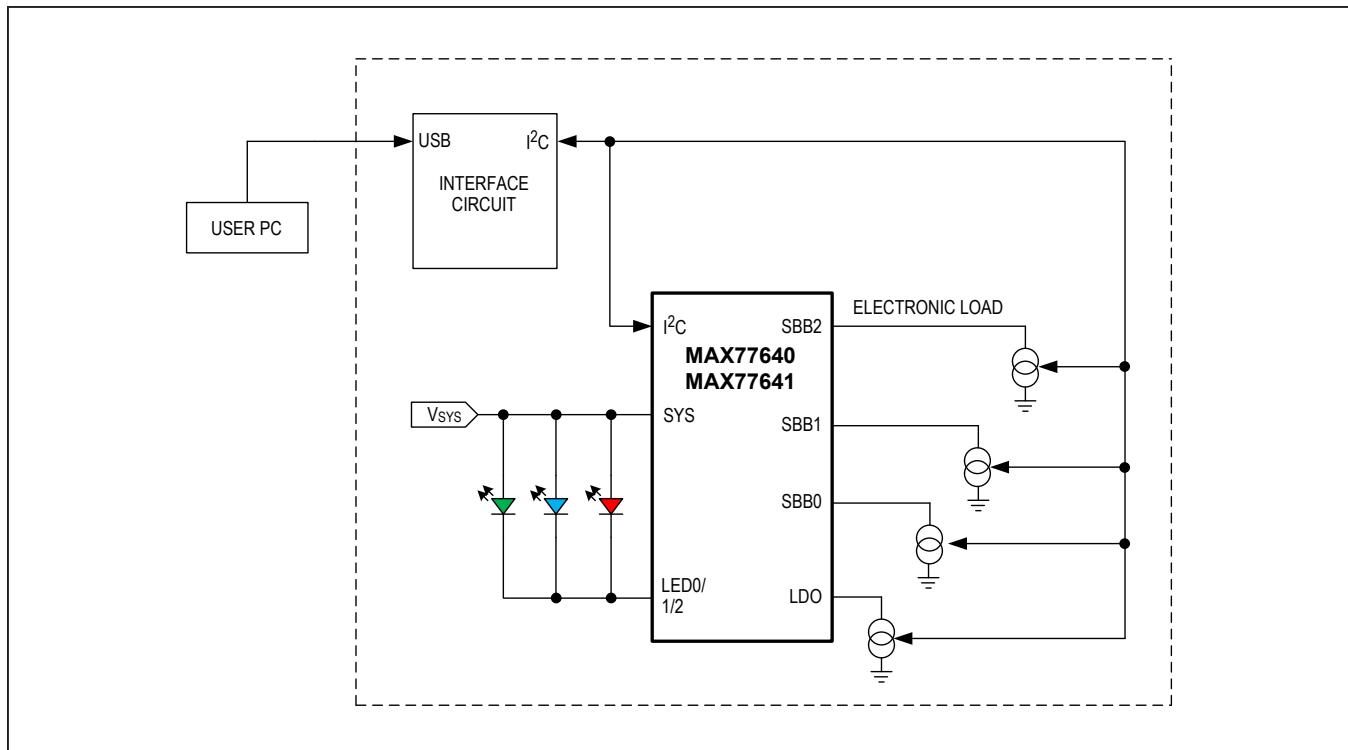


Figure 2. MAX77640/MAX77641 EV Kit Block Diagram

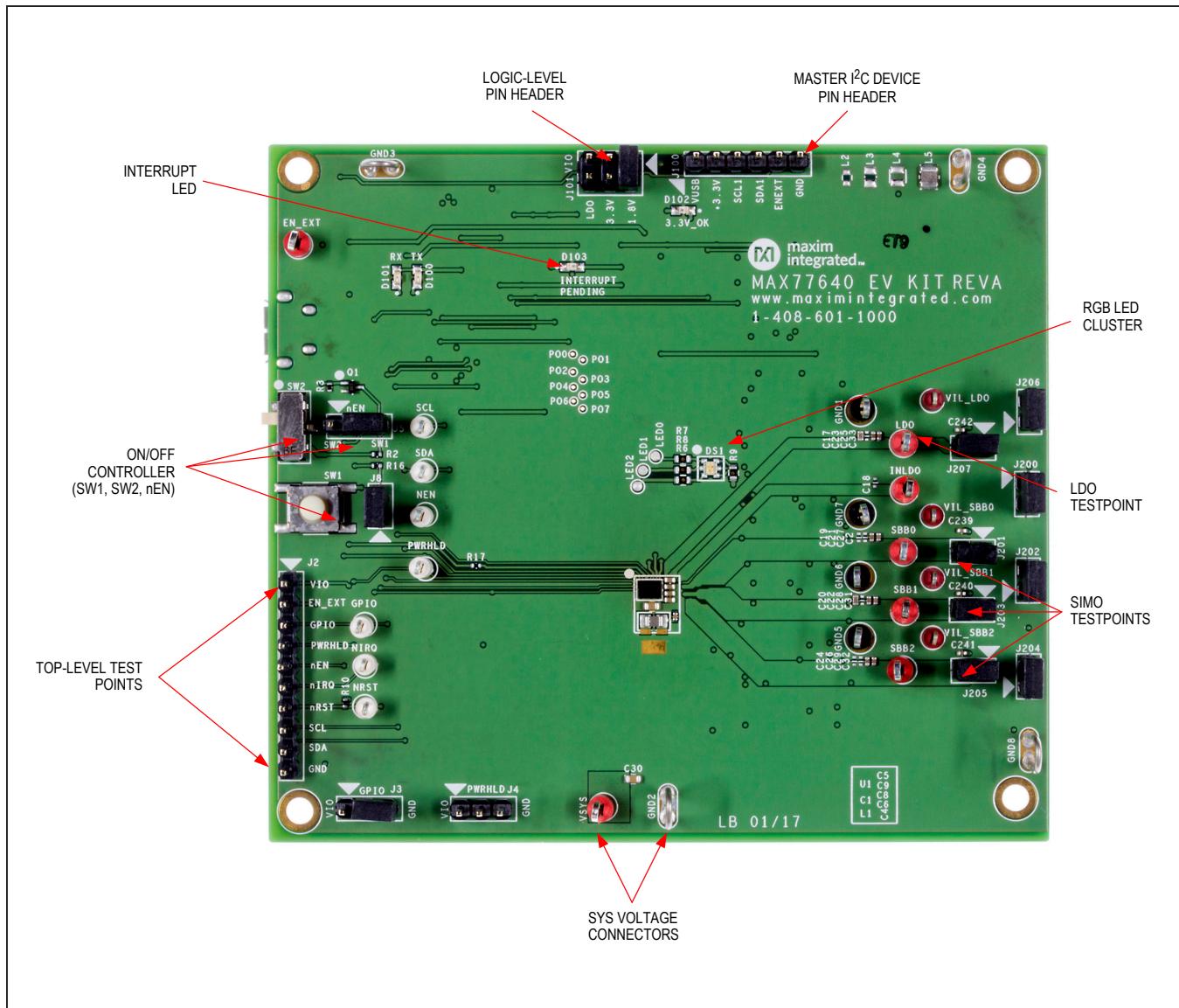


Figure 3. MAX77640/MAX77641 EV Kit Top View

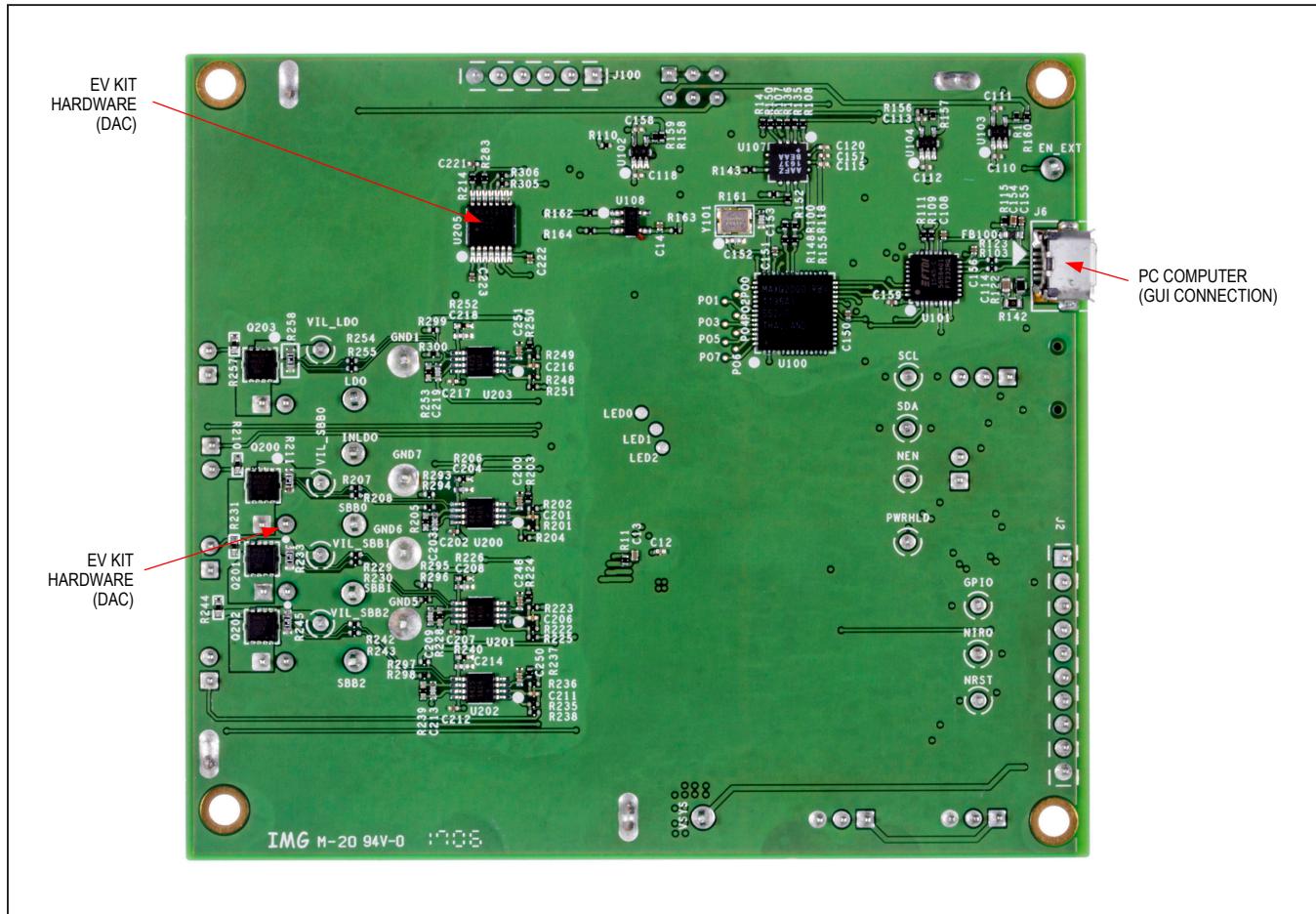


Figure 4. MAX77640/MAX77641 EV Kit Bottom View

**Table 1. Default Shunt Positions and Jumper Descriptions**

REFERENCE DESIGNATOR	DEFAULT POSITION	FUNCTION
J100	N/A	Do not connect shunts to J100.
J101	1-2	<b>1-2:</b> Connects a $V_{IO}$ to the 1.8V EV kit logic rail. <b>3-4:</b> Connects a $V_{IO}$ to the 3.3V EV kit logic rail. <b>5-6:</b> Connects a $V_{IO}$ to the LDO output.
J200	1-2	<b>1-2:</b> Connects the U200 amplifier to the gate of the Q200 load FET.
J201	1-2	<b>1-2:</b> Connects SBB0 to load cell.
J202	1-2	<b>1-2:</b> Connects the U202 amplifier to the gate of the Q202 load FET.
J203	1-2	<b>1-2:</b> Connects SBB1 to load cell.
J204	1-2	<b>1-2:</b> Connects the U204 amplifier to the gate of the Q204 load FET.
J205	1-2	<b>1-2:</b> Connects SBB2 to load cell.
J206	1-2	<b>1-2:</b> Connects the U206 amplifier to the gate of the Q206 load FET.
J207	1-2	<b>1-2:</b> Connects LDO to load cell.
J2	N/A	Do not connect shunts to J2.
J3	1-2	<b>1-2:</b> Connects GPIO to $V_{IO}$ . <b>2-3:</b> Connects GPIO to GND.
J4	1-2	<b>1-2:</b> Connects PWRHLD to $V_{IO}$ . <b>2-3:</b> Connects PWRHLD to GND.
J5	2-3	<b>1-2:</b> Connects nEN to SW2. <b>2-3:</b> Connects nEN to SW1.
J6	N/A	USB-Micro adapter for communications to a user PC (and use the GUI).
J8	1-2	<b>1-2:</b> Connects nRST to PWRHLD through a $150\Omega$ resistor.

## Quick Start

Follow this procedure to familiarize yourself with the EV kit.

**Note:** In the following sections, software-related items are identified by bolding. Text in **bold** refers to items directly from the EV kit software. Text in **bold and underlined** refers to items from the Windows operating system.

## Required Equipment

- MAX77640/MAX77641 EV kit
- Windows-based PC
- Power supply
- Ammeter
- DVM
- Micro-USB cable
- GUI

## Procedure

The EV kit is fully assembled and tested. Follow the steps below to verify board operation:

- 1) Visit the product webpage at [www.maximintegrated.com/max77640evkit](http://www.maximintegrated.com/max77640evkit) and navigate to Design Resources to download the latest version of the EV kit software. Save the EV kit software to a temporary folder and decompress the ZIP file.
- 2) Make sure shunts are installed on jumpers per [Figure 1](#).
- 3) Connect a Micro-B USB cable between the EV kit's J6 and your Windows-based PC.
- 4) Apply a 3.7V supply (set for 100mA current limit) through an ammeter (set for 10mA range) across the SYS and GND2 terminals of the EV kit. Turn the supply on.
- 5) Open the GUI and press the **Connect** button in the upper left corner. Wait for the device to respond, and in the **Synchronize** window, press the **Read and close** button.

- 6) Press the on-key (SW1).
- 7) Confirm with the ammeter that the quiescent current is approximately 40 $\mu$ A.
- 8) Connect a DVM to LDO, SBB0, SBB1, and SBB2. For the MAX77640A, 1.5V, 1.8V, 1.2V, and 3.3V appear, respectively. For the MAX77641A, 1.5V, 1.8V, 3.3V, and 5.0V appear, respectively.

This concludes the Quick Start procedure. Users are now encouraged to explore the device and its register settings with the GUI. During general device evaluation, set the ammeter range to greater than or equal to 1A to minimize the impact of its series resistance.

For more information on the GUI, see the [Software](#) section.

## EV Kit Features

### On-Key Options

For applications that require a user switch to enable the IC, the EV kit comes with two common types: the push-button (momentary) and the slider-switch (persistent). The active-low enable pin (nEN) has an external pullup resistor R2. Select which type of switch to use with a

jumper on J5. See the data sheet for more information on configuring the IC for momentary or persistent switches.

### Electronic Load

To easily evaluate the SIMO and LDO, the EV kit comes with an on-board, electronic load controllable with the GUI. Through I<sup>2</sup>C, the GUI controls an on-board DAC and op-amp configuration to set the load current. Jumpers on J201, J203, J205, and J207 connect the load to the outputs of the SBB0, SBB1, SBB2, and LDO, respectively. Emulate SYS loading by removing the jumper on J207 and connecting pin 1 of J207 to V<sub>SYS</sub>. See the [Software](#) section for how to set the load current from the GUI.

To simulate load transient response, connect a signal generator to pin 2 of J200 (SBB0), J202 (SBB1), J204 (SBB2), or J206 (LDO). Drive the MOSFET gate with an analog signal between 1V (off) and 3V (fully on) to apply transients to the output of the SIMO or LDO. For manually measuring current, measure the voltage across the sense resistor using test point V<sub>I</sub>L\_SBB0, V<sub>I</sub>L\_SBB1, V<sub>I</sub>L\_SBB2, or V<sub>I</sub>L\_LDO. Since the resistor is 1 $\Omega$ , the voltage to load current conversion is 1:1.

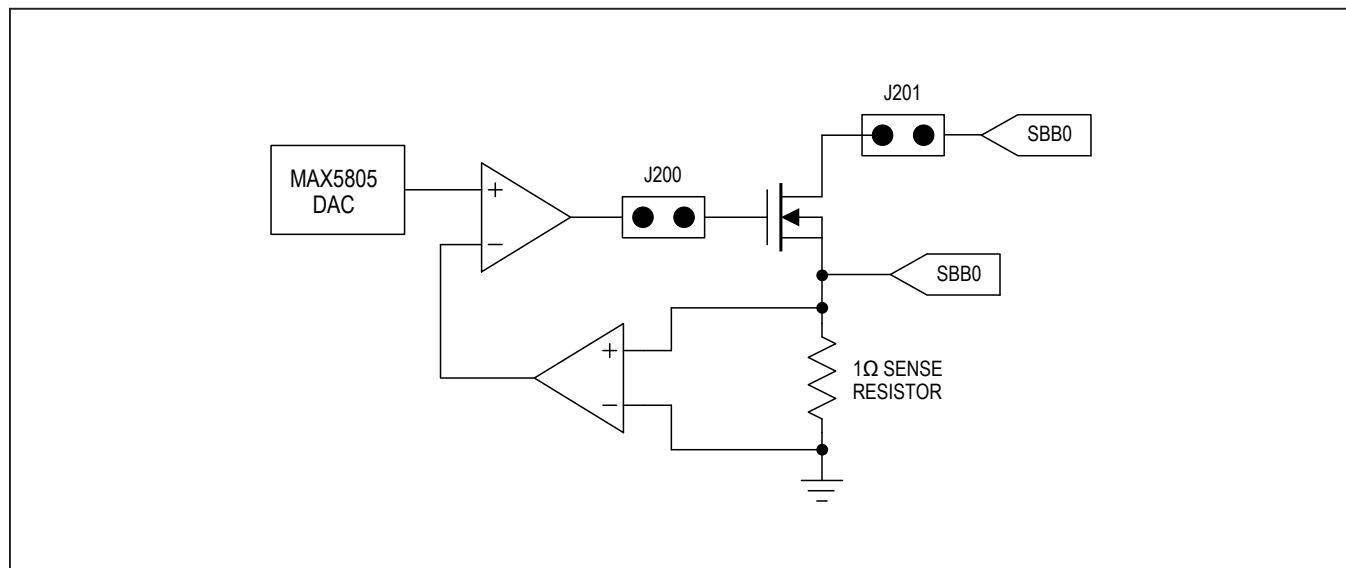


Figure 5. Electronic Load Block Diagram

## Software

The graphical user interface (GUI) software allows for quick, easy, and thorough evaluation of the MAX77640/MAX77641.

The GUI is designed to have individual tabs for each functional block of the device (MAX77640/MAX77641), Interrupts/Status, SIMO, and an additional tab for controlling EV kit hardware (Load Control). See [Figure 6](#) for a screenshot of the GUI upon first opening.

## Installation

Visit the product webpage at [www.maximintegrated.com/max77640evkit](http://www.maximintegrated.com/max77640evkit) and navigate to *Design Resources* to download the latest version of the EV kit software. Save the EV kit software to a temporary folder and decompress the ZIP file.

## Windows Drivers

Upon connection of a micro-USB cable between your PC and the EV kit for the first time, wait a few minutes for Windows to automatically install the necessary drivers.

## Graphical User Interface (GUI) Details

The GUI drives I<sup>2</sup>C communication with the EV kit. Every control in the GUI (excluding the Load Control) corresponds directly to a register within the MAX77640/MAX77641. Refer to the [AN6516: MAX77640/MAX77641 Programmer's Guide](#) for a complete description of the registers. The Load Control and tabs provide additional functionality with the EV kit.

### Load Control Tab

The **Load Control** tab contains controls for setting load on the SIMO outputs. The GUI is capable of setting steady-state, transient, and random load currents. To set a load current, use the slider bar or text field to input a value (mA) and press the **Enable** button. Shuffle through the modes to exercise different load conditions. The offset and gain values are set by Maxim and do not need to be altered.

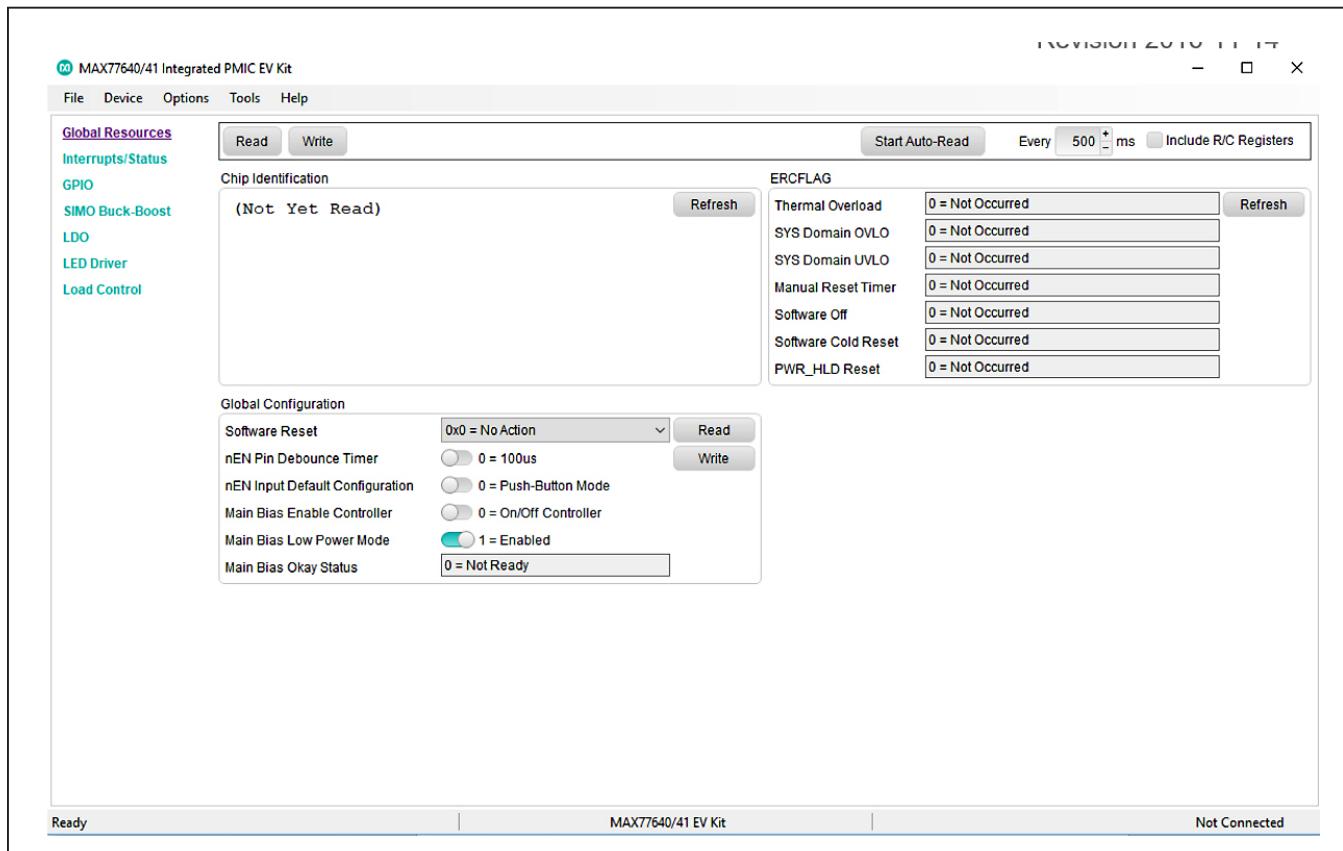


Figure 6. MAX77640/MAX77641 EV Kit GUI Top-Level Interface



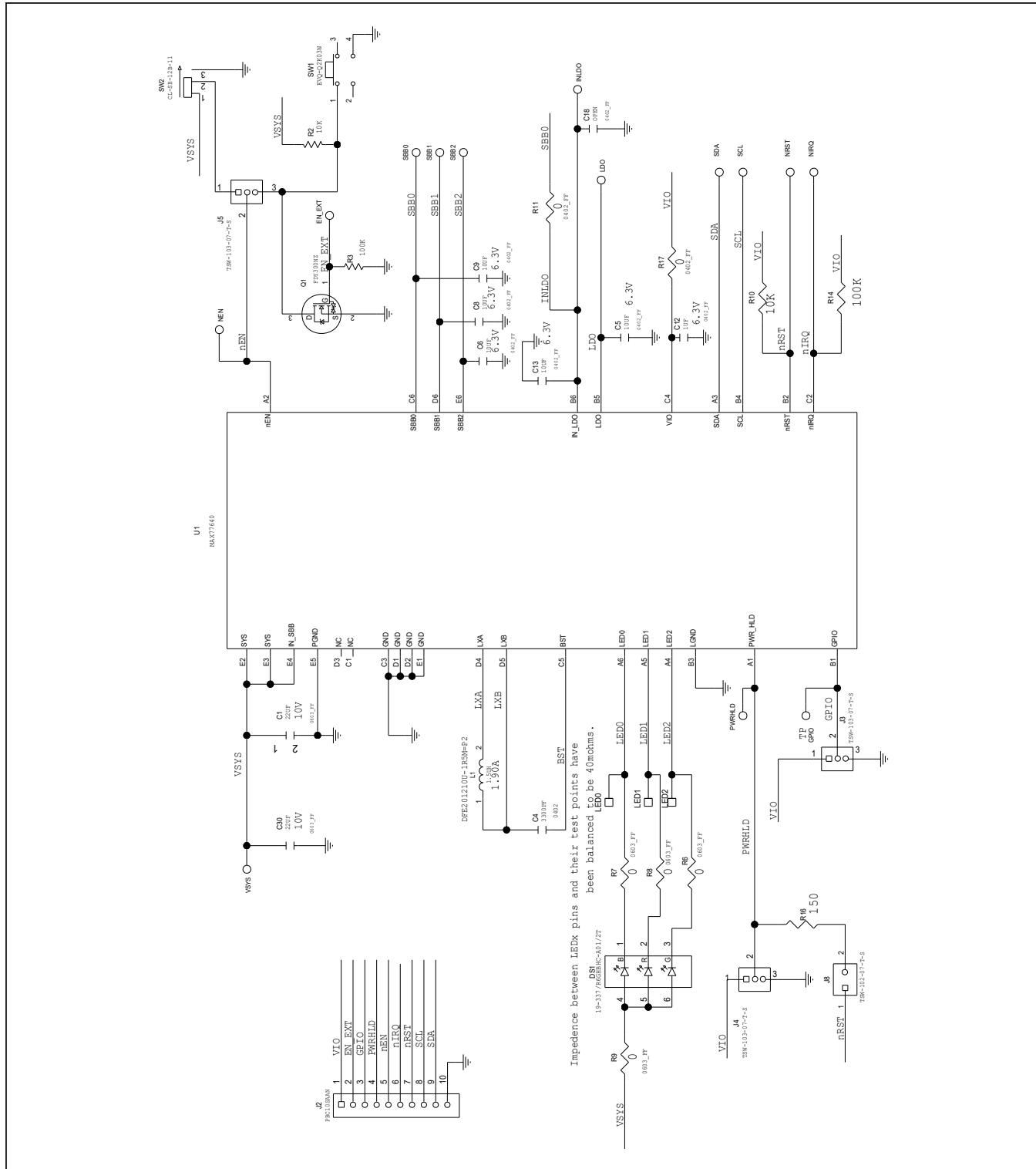


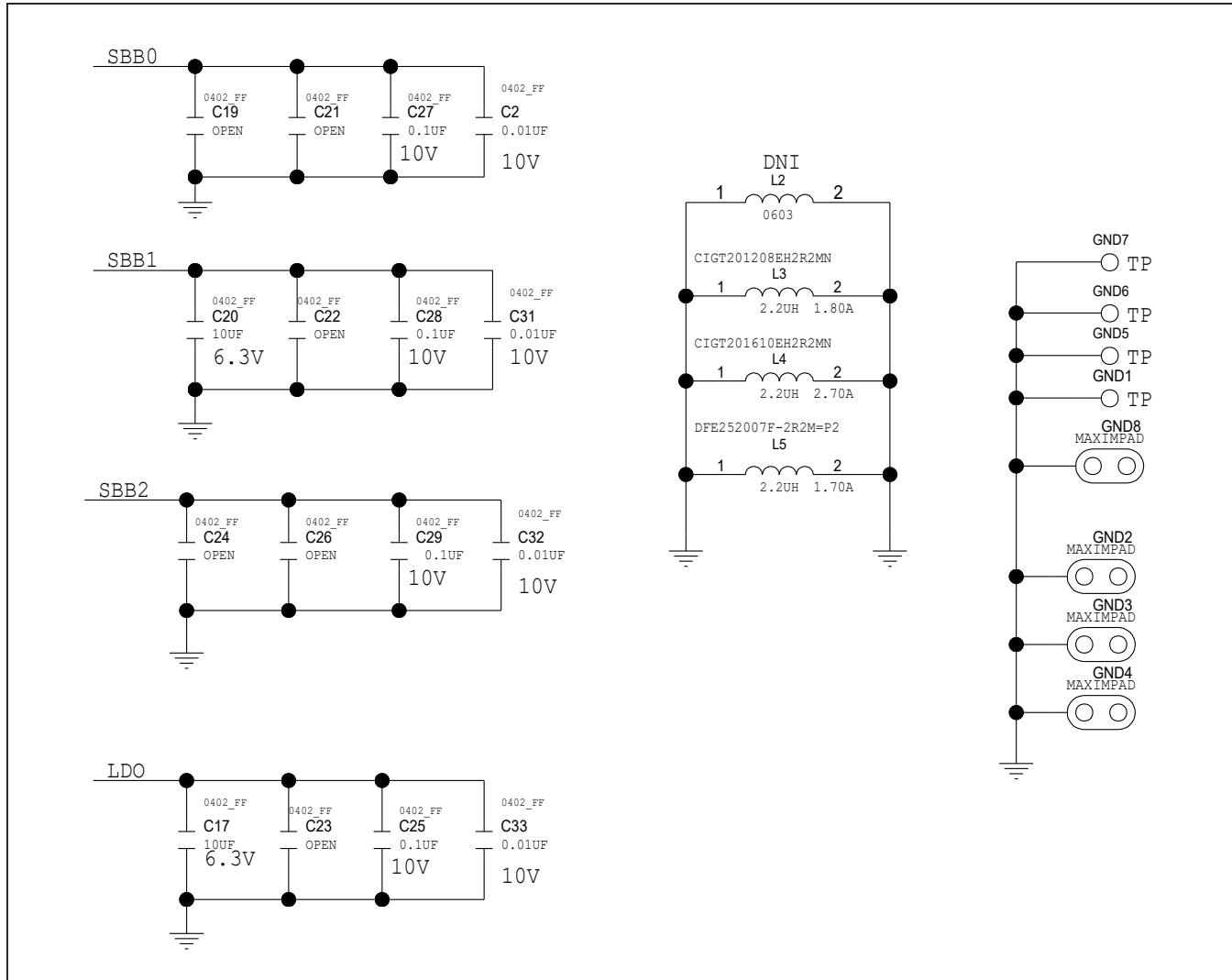
**MAX77640/MAX77641 EV Kit Schematic**

Part Number	Configuration	7-bit	8-bit Write	8-bit Read
MAX77640 (PMIC)	ADDR OTP bit set for 0	0x40 0b100 0000	0x80 0b1000 0000	0x81 0b1000 0001
MAX77640 (PMIC)	ADDR OTP bit set for 1	0x48 0b100 1000	0x90 0b1001 0000	0x91 0b1001 0001
MAX77640 (PMIC)	Maxim internal test mode	0x49 0b100 1001	0x92 0b1001 0010	0x93 0b1001 0011
MAX5815 (DAC)	ADDR1=ADDR0=GND	0x1F 0b001 1111	0x3E 0b0011 1110 0x10* 0b0001 0000	0x3F 0b0011 1111
24AA02 (EEPROM)	N/A	0x50 to 0x57 0b1010xxx	0b1010xxx0	0b1010xxx1

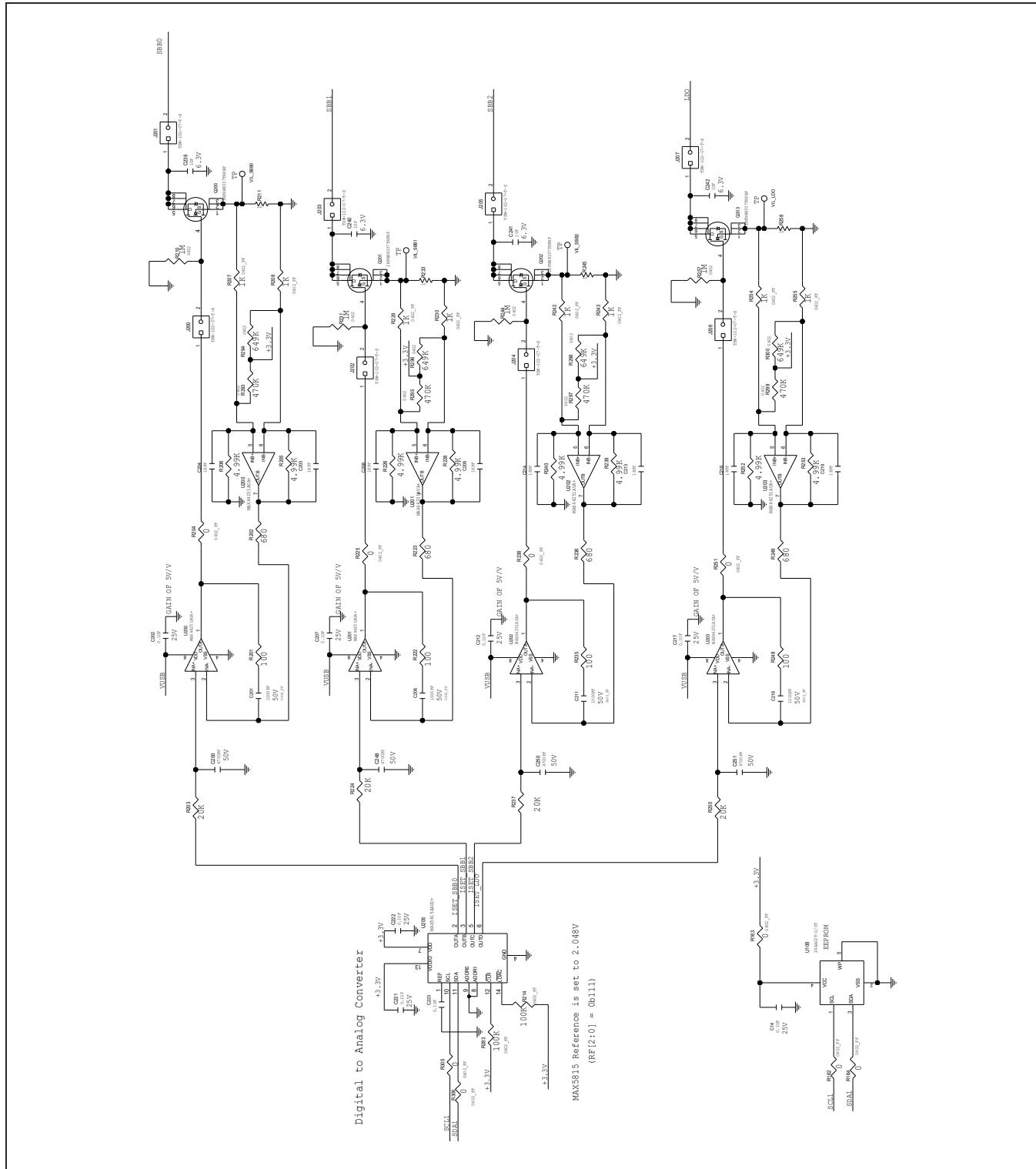
\*MAX5815 ALSO RESPONDS TO AN I2C BROADCAST ADDRESS 0b0001 0000

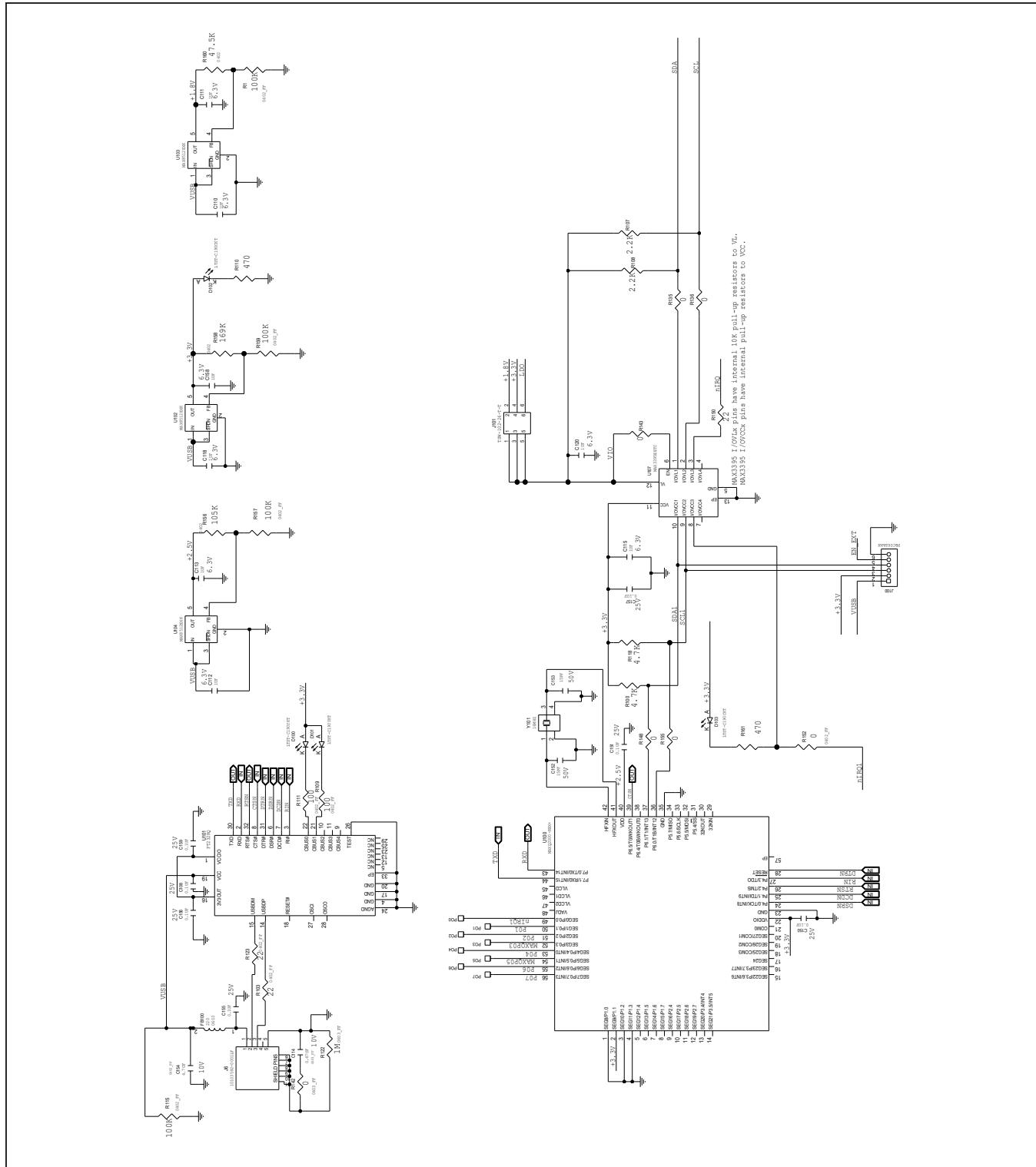
## MAX77640/MAX77641 EV Kit Schematic (continued)

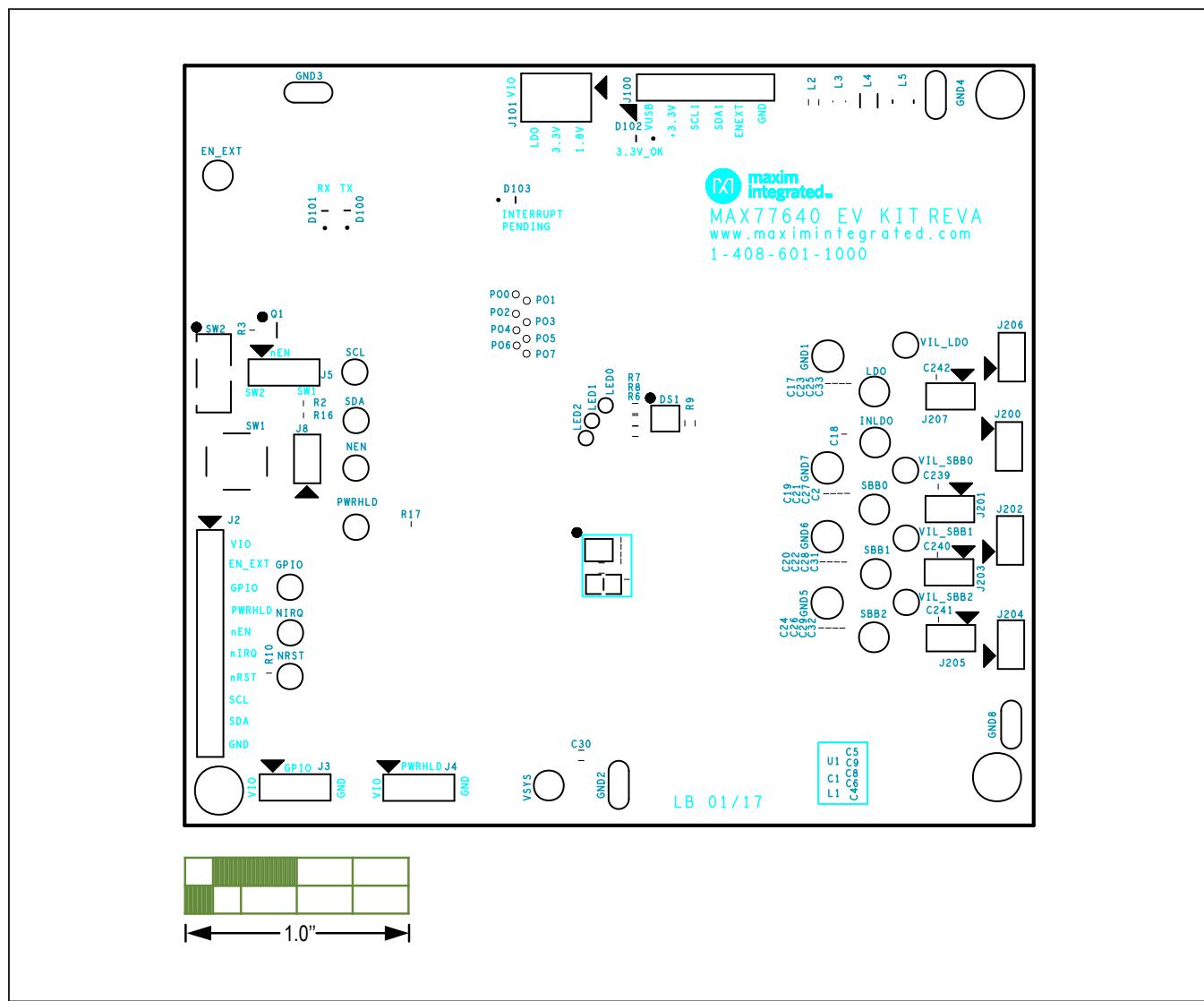


**MAX77640/MAX77641 EV Kit Schematic (continued)**

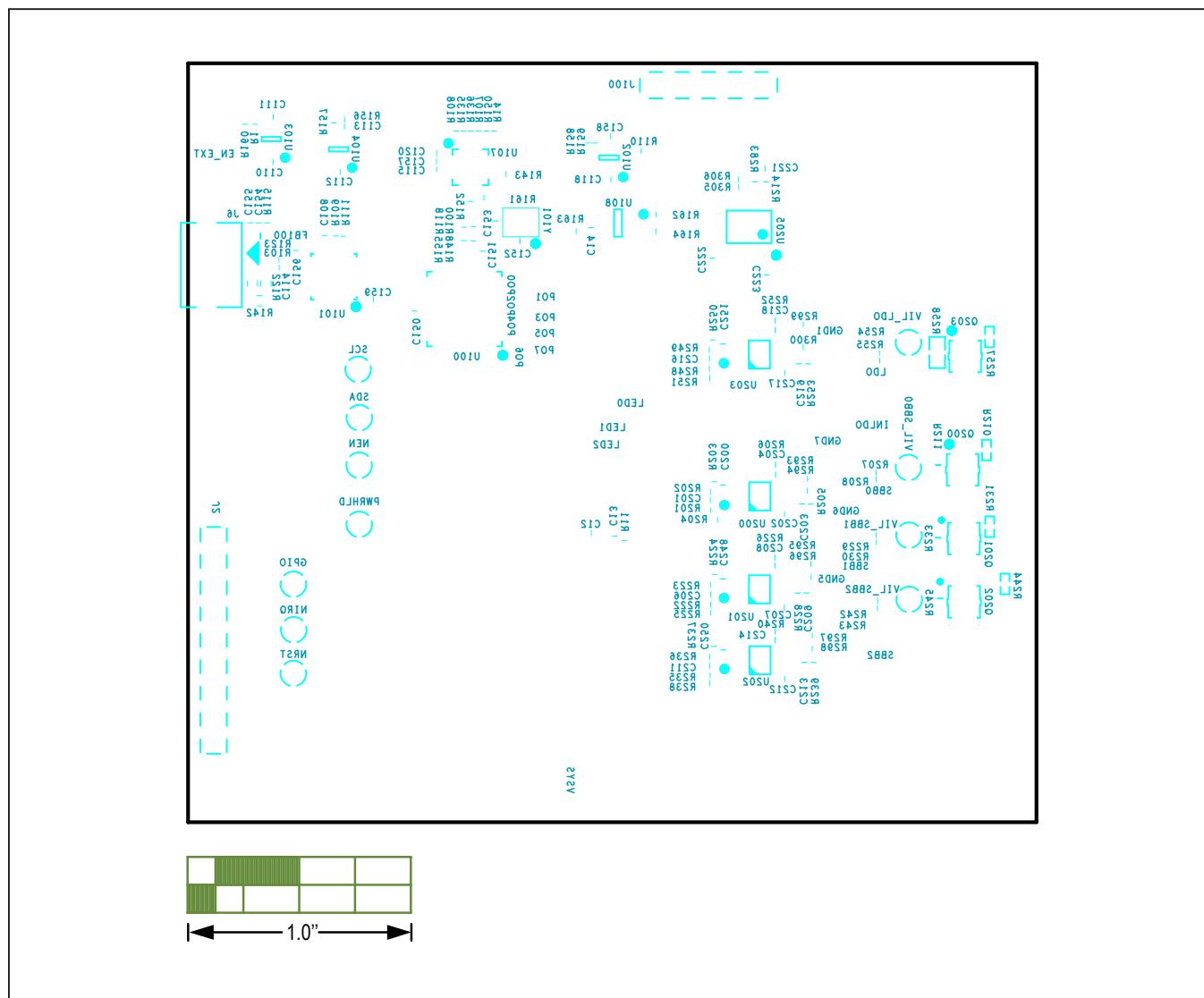
## MAX77640/MAX77641 EV Kit Schematic (continued)



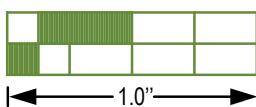
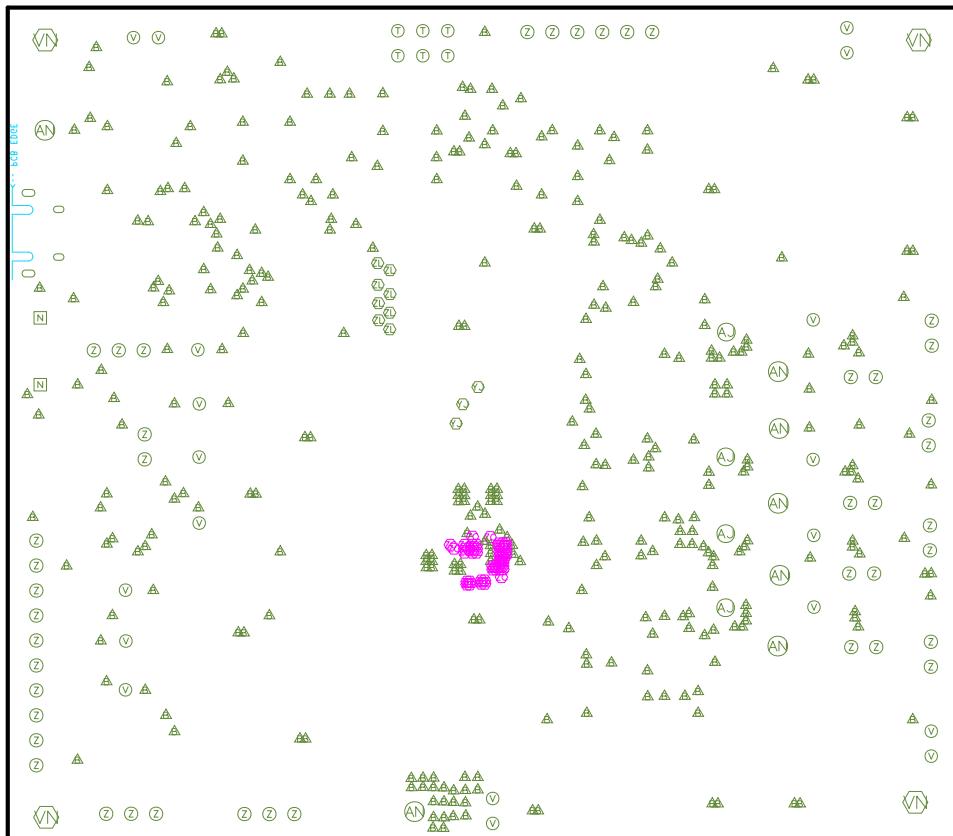
**MAX77640/MAX77641 EV Kit Schematic (continued)**

**MAX77640/MAX77641 EV Kit PCB Layouts**

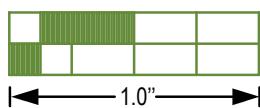
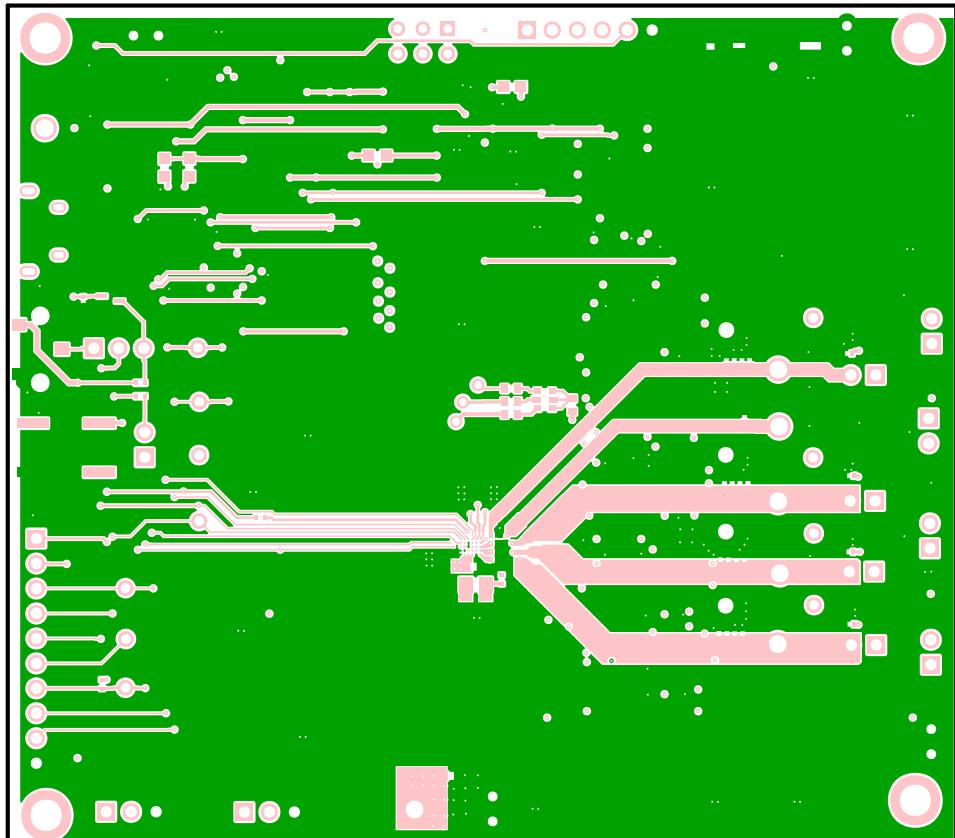
MAX77640/MAX77641 EV Kit Component Placement Guide—Top Silkscreen

**MAX77640/MAX77641 EV Kit PCB Layouts (continued)**

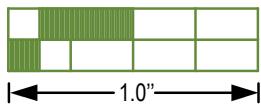
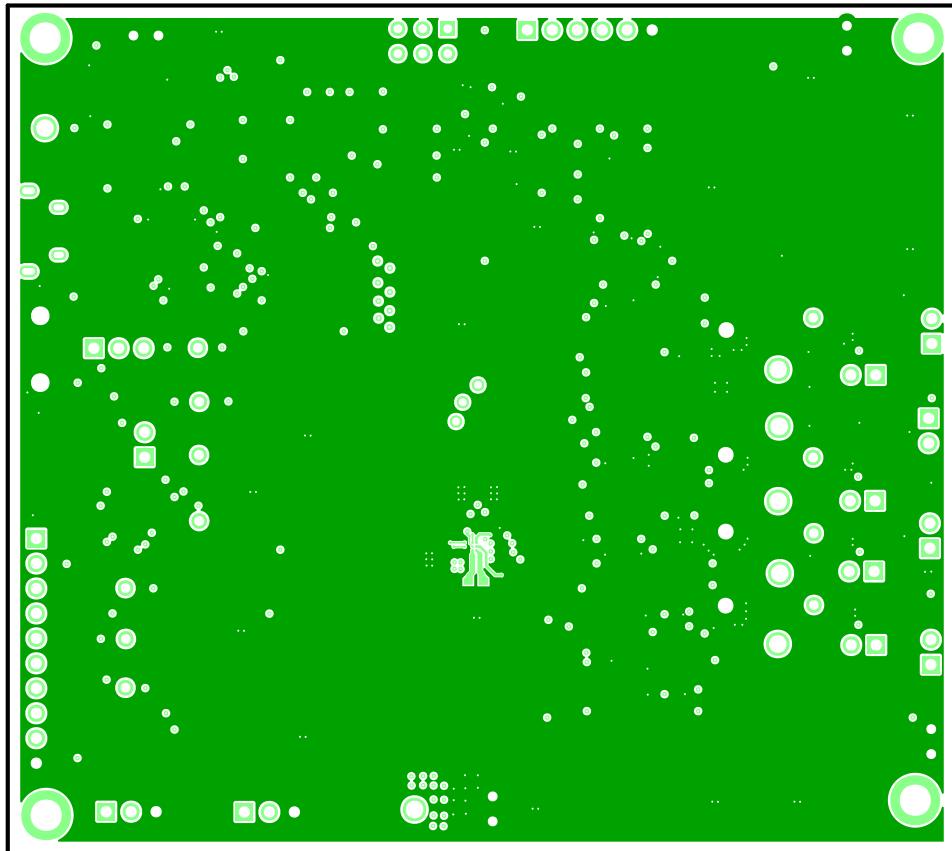
MAX77640/MAX77641 EV Kit Component Placement Guide—Bottom Silkscreen

**MAX77640/MAX77641 EV Kit PCB Layouts (continued)**

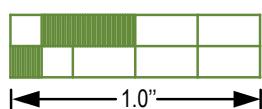
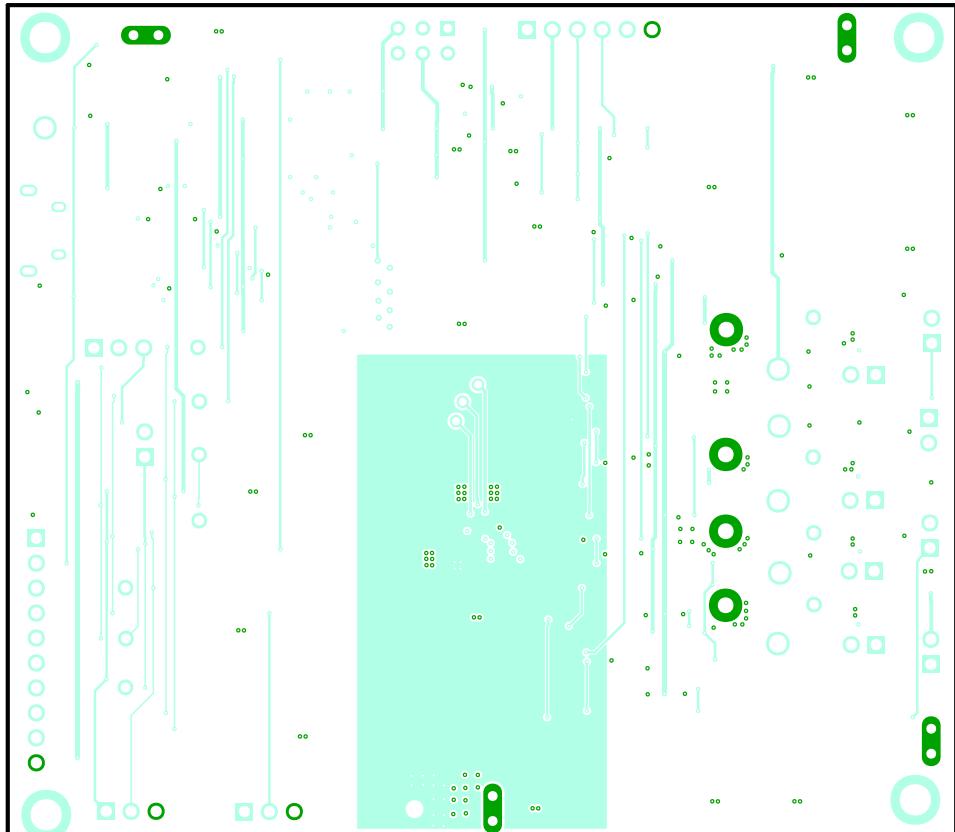
MAX77640/MAX77641 EV Kit PCB Layout—Fab Notes

**MAX77640/MAX77641 EV Kit PCB Layouts (continued)**

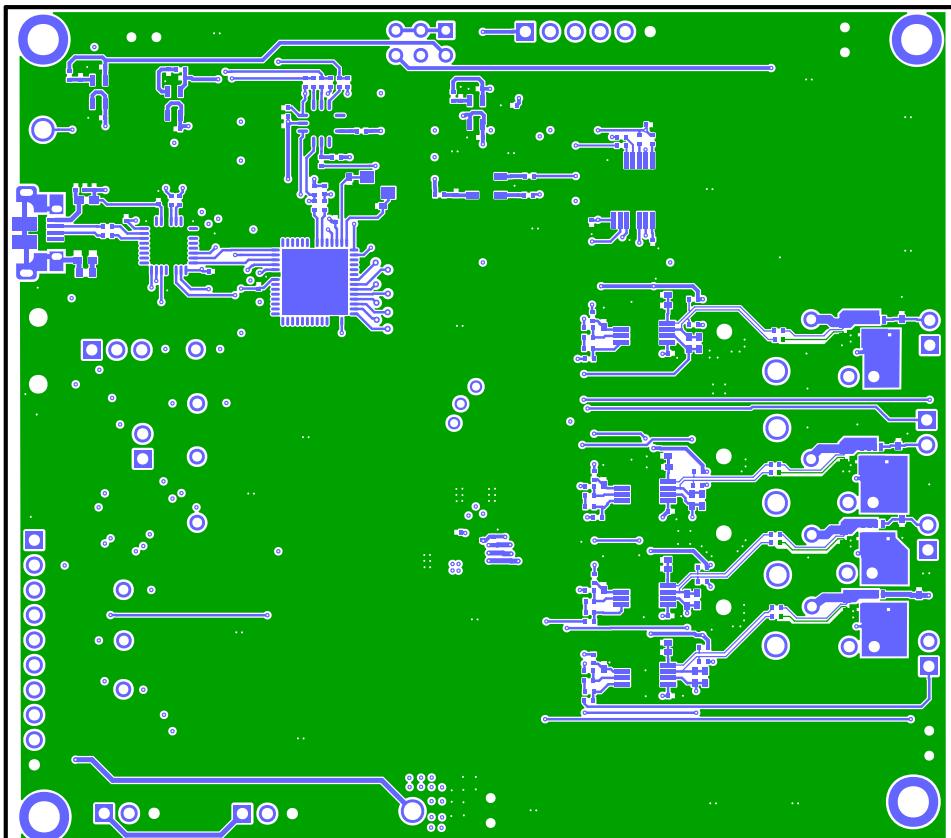
MAX77640/MAX77641 EV Kit PCB Layout—Top Layer

**MAX77640/MAX77641 EV Kit PCB Layouts (continued)**

MAX77640/MAX77641 EV Kit PCB Layout—Internal Layer 2

**MAX77640/MAX77641 EV Kit PCB Layouts (continued)**

MAX77640/MAX77641 EV Kit PCB Layout—Internal Layer 3

**MAX77640/MAX77641 EV Kit PCB Layouts (continued)**

MAX77640/MAX77641 EV Kit PCB Layout—Bottom

## Revision History

REVISION NUMBER	REVISION DATE	DESCRIPTION	PAGES CHANGED
0	5/18	Initial release	—

For pricing, delivery, and ordering information, please contact Maxim Direct at 1-888-629-4642, or visit Maxim Integrated's website at [www.maximintegrated.com](http://www.maximintegrated.com).

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